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Attorney's Docket No. 5288.P002

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:)

Varaprasad Vajjhala, et al.)

Examiner: Not Yet Assigned

Application No.: 09/977,511)

Art Unit: 2151

Filed: October 12, 2001)

For: METHOD AND SYSTEM TO)
ALLOCATE RESOURCES WITHIN AN)
INTERCONNECT DEVICE ACCORDING)
TO A RESOURCE ALLOCATION TABLE)

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Assistant Commissioner for Patents, Washington, D. C. 20231 on September 24, 2002.
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Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

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 A statement pursuant to 37 C.F.R. §1.97(e) or

 A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

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- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 07/24/2002



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| Substitute for Form 1449A/PTO (Modified) | | Complete if Known | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary) | | Application Number | 09/977,511 |
| | | Filing Date | October 12, 2001 |
| | | First Named Inventor: | Varaorasad Vajhala, et al. |
| | | Group Art Unit | 2151 |
| | | Examiner Name | Not Yet Assigned |
| | | Attorney Docket Number | 5288.P002 |
| Sheet | 1 | | |
| OTHER ART - NO PATENT LITERATURE DOCUMENTS | | | |
| Examiner Initials* | Cite No ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published | T ² |
| | | "InfiniBand Switch Chip Runs at 10 Gbps On Eight Ports ", Nicholas Cravotta, 11/8/2001, EDN, 1 page | |
| | | "Assemble Fast Switch Fabrics With 32-Port InfiniBand Node p. 60", Electronic Design, 10/15/2001, 4 pages | |
| | | "RedSwitch, Inc. Announces Industry's Highest Performance and Highest Integration InfiniBand Switch Chip", RedSwitch Press Release, 10/16/2001, 2 pages. | |
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| | | "RedSwitch and Agilent Technologies Unveil 160-GB/s Throughout Switch Product for InfiniBand Architecture", RedSwitch Press Release, Intel Developer Forum Conference, San Jose, Calif., Feb. 27, 2001, Mark Alden-Agilent, Tony Chance-RedSwitch, 2 pages. | |
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